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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of: Max M. Yeung et al.  
Serial No.: 09/975,293  
Title: CONSTRUCTION OF AN OPTIMIZED SEC-DED CODE AND  
LOGIC FOR SOFT ERRORS IN SEMICONDUCTOR  
MEMORIES  
Filed: October 11, 2001  
Attorney Docket No.: 01-322 / 1496.00144  
Examiner: Torres, J.  
Art Unit: 2133  
In Response To: Office Action mailed September 6, 2005

**CERTIFICATE OF MAILING**

I hereby certify that this letter, the response or amendment attached hereto are being deposited with the United States Postal Service as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 5, 2005.

By: Jan M. Dunbar  
Jan M. Dunbar

**RESPONSE TO ELECTION/RESTRICTION REQUIREMENT  
UNDER 35 U.S.C. §121**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**R E M A R K S**

Applicant's representative provisionally elects with traverse claims 1-13 and 21, Group I. Applicant's representative respectfully requests that the Examiner reconsider and withdraw the restriction requirement.

Applicants' representative (Chris Maiorana) and Examiner Decady held a telephone interview on October 5, 2005. Examiner